

REMARKS

I. Claim Objections

The objections to claims 3, 10, 16, 23, 29, and 36 have been addressed by the amendments to the claims.

II. Rejections Under 35 U.S.C. § 112

The rejections to claims 5, 18, and 31 have been addressed by the amendments to the claims.

III. Rejections Under 35 U.S.C. § 103

Claims 1-2, 4-5, 9, 14-15, 17-18, and 22 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over *Kajihara et al.* (U.S. Patent No. 6,677,923) in view of *Nakano et al.* (U.S. Patent Application Publication No. 2002/0158859).

Claims 27-28, 30-31 and 35 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over *Kajihara* in view of *Nakano* and *Millman et al.* (U.S. Patent Application Publication No. 2002/0075251).

Claims 6 and 19 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over *Kajihara* in view of *Nakano* and further in view of *Odake et al.* (U.S. Patent No. 6,426,595).

Claims 11-13, and 24-26 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over *Kajihara* in view of *Nakano* and further in view of *Takeda et al.* (JP 09120934).

Claim 32 stands rejected under 35 U.S.C. § 103(a) as being unpatentable over *Kajihara* in view of *Nakano* and *Millman* and further in view of *Odake*.

Claims 37-39 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over *Kajihara* in view of *Nakano* and *Millman* and further in view of *Takeda*.

In response, these rejections are moot since the claims have been amended. In general, the prior art that relies on LCD technology uses row and column voltages to drive at least one plate of a capacitor to have a variable voltage. The voltage applied to that one plate is varied so as to vary the voltage across the capacitor spanning the liquid crystal material in an analog manner, which in turn causes the desired light transmittance (i.e., tone) emitted at a pixel to vary.

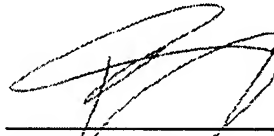
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See *Kajihara*, col. 2, lines 24-58. The amended claims are different in that they simultaneously apply one drive voltage (first drive voltage) to all the rows (one plate of the capacitor), and apply one of two drive voltages (second drive voltage and third drive voltage) to all of the columns (other plate of the capacitor), which is clearly not in an analog manner as in *Kajihara*. Furthermore, the claimed invention selects a single row by changing its impedance from high to low for the duration of a time cycle.

Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,



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